

Computer Architecture

Discussion Session

ETH Zürich

Fall 2019

1 November 2019

Agenda for Today

- Preparation of Mid-Term Exam – Fall 2019
 - DRAM Refresh – Utilization
 - Caching vs. Processing-in-Memory
 - DRAM Scheduling and Latency
 - RowHammer Mitigations
 - Genome Analysis
 - Processing-in-Memory: Ambit I