

Computer Architecture

Discussion Session 1

Prof. Onur Mutlu

ETH Zürich

Fall 2021

22 November 2021

Agenda for the Discussion Session

- Preparation of Final Exam – Fall 2021
 - HW1 Q3 – DRAM Refresh - Utilization
 - HW2 Q4 – Caching vs. Processing-in-Memory
 - HW1 Q5 from Fall 2020 – VRL: Variable Refresh Latency
 - HW2 Q2.2 from Fall 2020 – RowHammer Mitigations
 - HW3 Q2 from Fall 2020 – Genome Analysis I

Computer Architecture

Discussion Session 1

Prof. Onur Mutlu

ETH Zürich

Fall 2021

22 November 2021