

# Computer Architecture

## Discussion Session 2

Prof. Onur Mutlu

ETH Zürich

Fall 2021

4 December 2021

# Agenda for the Discussion Session

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- Preparation of Final Exam – Fall 2021
  - HW1 Q5 from Fall 2021 - DRAM Refresh Utilization
  - HW3 Q4 from Fall 2021 - Memory Scheduling
  - HW3 Q5 from Fall 2021 - DRAM Scheduling and Latency
  - HW3 Q6 from Fall 2021 - Emerging Memory Technologies
  - HW2 Q2 from Fall 2018 - Main Memory Organization and Interleaving
  - HW2 Q4 from Fall 2017 - Main Memory Potpourri
  - HW2 Q5 from Fall 2017 - DRAM Banks
  - HW2 Q6 from Fall 2017 - Memory Hierarchy
  - HW2 Q7 from Fall 2017 - Prefetching
  - Final Exam Q1 from Fall 2017 - DRAM Refresh
  - Final Exam Q8 from Fall 2017 - Prefetching

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