

Computer Architecture

Discussion Session 3

Prof. Onur Mutlu

ETH Zürich

Fall 2021

4 December 2021

Agenda for the Discussion Session

- Preparation of Final Exam – Fall 2021
 - HW2 Q2 from Fall 2021 - In-DRAM Bitmap Indices
 - HW4 Q3 from Fall 2021 - BossMem
 - HW4 Q6 from Fall 2021 - Cache Coherence
 - HW5 Q4 from Fall 2020 - Runahead Execution
 - HW5 Q8 from Fall 2020 - Building Multicore Processors
 - HW5 Q5 from Fall 2020 - Prefetching
 - HW3 Q3 from Fall 2020 - Genome Analysis
 - Final Exam Q8 from Fall 2019 - Memory Consistency

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