

# Design of Digital Circuits

## Discussion Session

Prof. Onur Mutlu

ETH Zurich

Spring 2018

1 June 2018

# Agenda for Today

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- HW1
  - 1. DRAM Refresh
  - 2. Main Memory Potpourri
- HW3
  - 2. The MIPS ISA
  - 3. Data Flow Programs
- HW4
  - 1. Tomasulo's Algorithm
- HW5
  - 4. GPUs and SIMD I

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