

Design of Digital Circuits

Lab 1 Supplement

Prof. Onur Mutlu

ETH Zurich

Spring 2018

6 March 2018

What We Will Learn?

- Boolean Equations

- Logic operations with binary numbers

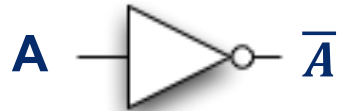
- Logic Gates

- Basic blocks that are interconnected to form larger units that are needed to construct a computer

Boolean Equations and Logic Gates

Simple Equations: NOT / AND / OR

\bar{A} (reads “not A”) is 1 iff A is 0



A	\bar{A}
0	1
1	0

$A \cdot B$ (reads “A and B”) is 1 iff A and B are both 1



A	B	$A \cdot B$
0	0	0
0	1	0
1	0	0
1	1	1

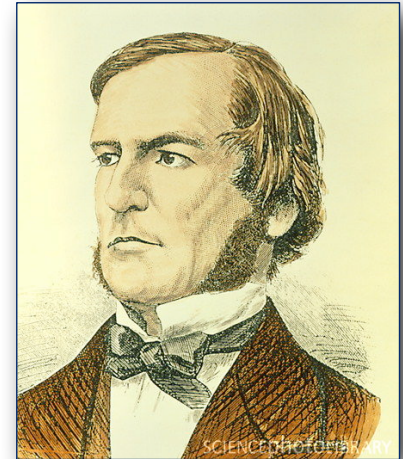
$A + B$ (reads “A or B”) is 1 iff either A or B is 1



A	B	$A + B$
0	0	0
0	1	1
1	0	1
1	1	1

Boolean Algebra: Big Picture

- An algebra on 1's and 0's
 - with AND, OR, NOT operations
- What you start with
 - **Axioms:** basic stuff about objects and operations you just assume to be true at the start
- What you derive first
 - **Laws and theorems:** allow you to manipulate Boolean expressions
 - ...also allow us to do some simplification on Boolean expressions
- What you derive later
 - More “sophisticated” properties useful for manipulating digital designs represented in the form of Boolean equations



Common Logic Gates

Buffer



A	Z
0	0
1	1

AND



A	B	Z
0	0	0
0	1	0
1	0	0
1	1	1

OR



A	B	Z
0	0	0
0	1	1
1	0	1
1	1	1

XOR



A	B	Z
0	0	0
0	1	1
1	0	1
1	1	0

Inverter



A	Z
0	1
1	0

NAND



A	B	Z
0	0	1
0	1	1
1	0	1
1	1	0

NOR



A	B	Z
0	0	1
0	1	0
1	0	0
1	1	0

XNOR



A	B	Z
0	0	1
0	1	0
1	0	0
1	1	1

Boolean Algebra: Axioms

<i>Formal version</i>	<i>English version</i>
1. <i>B</i> contains at least two elements, <i>0</i> and <i>1</i> , such that $0 \neq 1$	Math formality...
2. <i>Closure</i> $a, b \in B$, (i) $a + b \in B$ (ii) $a \bullet b \in B$	Result of AND, OR stays in set you start with
3. <i>Commutative Laws</i> : $a, b \in B$, (i) (ii)	For primitive AND, OR of 2 inputs, order doesn't matter
4. <i>Identities</i> : $0, 1 \in B$ (i) (ii)	There are identity elements for AND, OR, give you back what you started with
5. <i>Distributive Laws</i> : (i) (ii)	<ul style="list-style-type: none">• distributes over +, just like algebra ...but + distributes over •, also (!!)
6. <i>Complement</i> : (i) (ii)	There is a complement element, ANDing, ORing give you an identity

Boolean Algebra: Duality

- Interesting observation
 - All the axioms come in “dual” form
 - Anything true for an expression also true for its dual
 - So any derivation you could make that is true, can be flipped into dual form, and it stays true
- Duality -- More formally
 - A dual of a Boolean expression is derived by replacing
 - Every AND operation with... an OR operation
 - Every OR operation with... an AND
 - Every constant 1 with... a constant 0
 - Every constant 0 with... a constant 1
 - But don't change any of the literals or play with the complements!

Example

$$a \cdot (b + c) = (a \cdot b) + (a \cdot c)$$
$$\rightarrow a + (b \cdot c) = (a + b) \cdot (a + c)$$

Boolean Algebra: Useful Laws

Operations with 0 and 1:

1. $X + 0 = X$

2. $X + 1 = 1$

Dual



1D. $X \cdot 1 = X$

2D. $X \cdot 0 = 0$

AND, OR with identities
gives you back the original
variable or the identity

Idempotent Law:

3. $X + X = X$

3D. $X \cdot X = X$

AND, OR with self = self

Involution Law:

4. $\overline{\overline{X}} = X$

double complement =
no complement

Laws of Complementarity:

5. $X + \overline{X} = 1$

5D. $X \cdot \overline{X} = 0$

AND, OR with complement
gives you an identity

Commutative Law:

6. $X + Y = Y + X$

6D. $X \cdot Y = Y \cdot X$

Just an axiom...

Useful Laws (cont)

Associative Laws:

$$7. (X + Y) + Z = X + (Y + Z) \\ = X + Y + Z$$

$$7D. (X \cdot Y) \cdot Z = X \cdot (Y \cdot Z) \\ = X \cdot Y \cdot Z$$

Parenthesis order
doesn't matter

Distributive Laws:

$$8. X \cdot (Y + Z) = (X \cdot Y) + (X \cdot Z)$$

$$8D. X + (Y \cdot Z) = (X + Y) \cdot (X + Z) \quad \text{Axiom}$$

Simplification Theorems:

9.

9D.

10.

10D.

11.

11D.

Useful for
simplifying
expressions

Actually worth remembering — they show up a lot in real designs...

DeMorgan's Law

DeMorgan's Law:

$$12. \overline{(X + Y + Z + \dots)} = \bar{X} \cdot \bar{Y} \cdot \bar{Z} \cdot \dots$$

$$12D. \overline{(X \cdot Y \cdot Z \cdot \dots)} = \bar{X} + \bar{Y} + \bar{Z} + \dots$$

■ Think of this as a transformation

- Let's say we have:

$$F = A + B + C$$

- Applying DeMorgan's Law (12), gives us:

$$F = \overline{\overline{(A + B + C)}} = \overline{(\bar{A} \cdot \bar{B} \cdot \bar{C})}$$

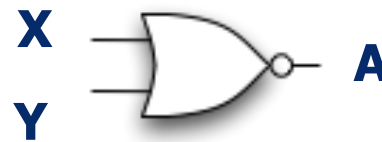
DeMorgan's Law (cont.)

Interesting — these are conversions between **different types of logic**

That's useful given you don't always have **every type of gate**

$$A = \overline{(X + Y)} = \bar{X}\bar{Y}$$

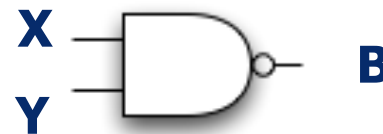
NOR is equivalent to AND with inputs complemented



X	Y	X + Y	X	Y	XY
0	0	1	1	1	1
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	0

$$B = \overline{(XY)} = \bar{X} + \bar{Y}$$

NAND is equivalent to OR with inputs complemented



X	Y	XY	X	Y	X + Y
0	0	1	1	1	1
0	1	1	1	0	1
1	0	1	0	1	1
1	1	0	0	0	0

Design of Digital Circuits

Lab 1 Supplement

Prof. Onur Mutlu

ETH Zurich

Spring 2018

6 March 2018