P&S Accelerating Genomics Lecture 5: GateKeeper





ETH Zurich Fall 2022 17 November 2022





Previous Lectures

Spring 2022 Meetings/Schedule

| Week | Date | Livestream | Meeting | | | |
|------|--------------|---------------|--|---|---|----|
| W1 | 8.3 Tue. | You Tube Live | M1: P&S Mobile Genomics Course Introduction & Project Proposals (PDF) mathematical (PPT) | | | |
| W2 | 15.3 Tue. | You Tube Live | M2: Introduction to Sequencing | stream - P&S Mutlu Lectures | Genome Sequencing on Mobile | ^ |
| W3 | 22.3 Tue. | You Tube Live | M3: Read Mapping | P&S Mobile Genomics | Mobile Genomics Course - | ≡+ |
| | | | | Dr. Mohammed Aler Tomatic and a second and | Meeting 1: Course Introduction Onur Mutlu Lectures | |
| | | | 2 | n n n n n n n n n n n n n n n n n n n | Mobile Genomics Course - Meeting 2: Introduction to Onur Mutlu Lectures | |
| | | | 3 | What makes read mapping a bott 1:01:32 | Mobile Genomics Course - Meeting 3: Read Mapping (Sprin Onur Mutlu Lectures | |

https://safari.ethz.ch/projects_and_seminars/spring2022/doku.php? id=genome_seq_mobile

Let's Review This Paper [Alser+, Bioinformatics 2017]

<u>Mohammed Alser</u>, Hasan Hassan, Hongyi Xin, Oguz Ergin, Onur Mutlu, and Can Alkan <u>"GateKeeper: A New Hardware Architecture for Accelerating Pre-Alignment in</u>

DNA Short Read Mapping"

Bioinformatics, [published online, May 31], 2017.

Source Code

[Online link at Bioinformatics Journal]

Bioinformatics



Article Navigation

GateKeeper: a new hardware architecture for accelerating pre-alignment in DNA short read mapping @

Mohammed Alser 🖾, Hasan Hassan, Hongyi Xin, Oğuz Ergin, Onur Mutlu 🖾, Can Alkan 🖾

Bioinformatics, Volume 33, Issue 21, 01 November 2017, Pages 3355–3363,

https://doi.org/10.1093/bioinformatics/btx342

Published: 31 May 2017 Article history •

GateKeeper: A New Hardware Architecture for Accelerating Pre-Alignment in DNA Short Read Mapping

Mohammed Alser, Hasan Hassan, Hongyi Xin, Oğuz Ergin, Onur Mutlu, Can Alkan **Bioinformatics, 2017**

Presented by: Mohammed Alser

TOBB UNIVERSITY OF ECONOMICS & TECHNOLOGY ETHZÜRICH Carnegie Mellon



Executive Summary

- Problem: Genomic similarity measurement is a computational bottleneck. Examining the similarity of highly-dissimilar genomic sequences consumes an overwhelming majority of a modern read mapper's execution time.
- <u>Goal</u>: Develop a fast and effective *filter* that can detect highly-dissimilar genomic sequences and eliminate them *before* invoking computationally costly alignment algorithms.
- Key observation: If two strings differ by *E* edits, then every pairwise match can be aligned in at most 2E shifts.
- Key ideas:
 - *Quickly* find similar sequences using *Hamming Distance*.
 - Compute "Shifted Hamming Distance" for the rest of sequence pairs: ANDing 2E+1 Hamming vectors of two strings, to identify dissimilar sequences.
 - Use only bit-parallel operations that nicely map to:
 - SIMD instructions, FPGA, Logic layer of the 3D-stacked memory, and In-memory accelerators (e.g., Ambit)

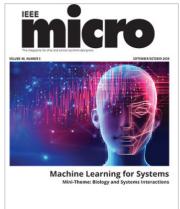
Key results:

 Provides a huge speedup of up to 130x compared to the previous state of the art software solution. We need intelligent algorithms and intelligent architectures that handle data well

Detailed Analysis of Tackling the Bottleneck

Mohammed Alser, Zülal Bingöl, Damla Senol Cali, Jeremie Kim, Saugata Ghose, Can Alkan, Onur Mutlu <u>"Accelerating Genome Analysis: A Primer on an Ongoing Journey"</u>

IEEE Micro, August 2020.





Home / Magazines / IEEE Micro / 2020.05

IEEE Micro

Accelerating Genome Analysis: A Primer on an Ongoing Journey

Sept.-Oct. 2020, pp. 65-75, vol. 40 DOI Bookmark: 10.1109/MM.2020.3013728

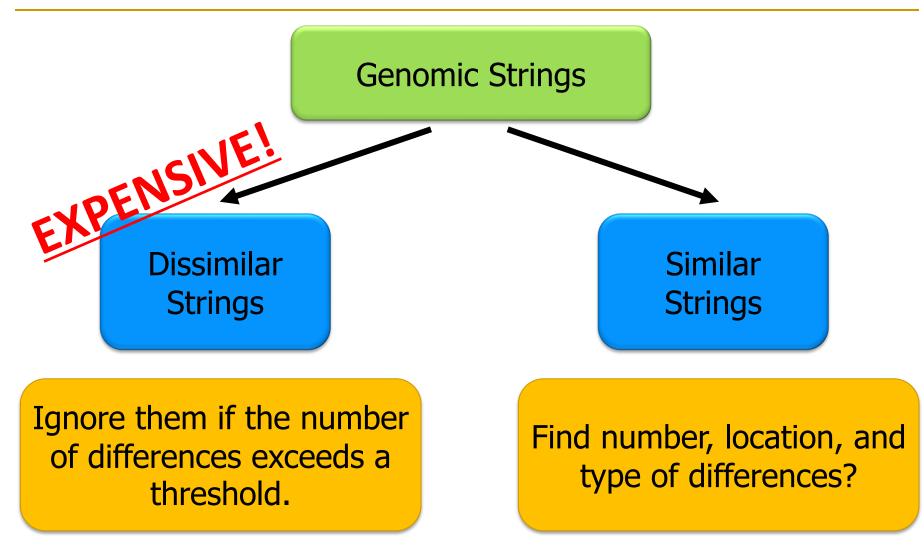
Authors

Mohammed Alser, ETH Zürich Zulal Bingol, Bilkent University Damla Senol Cali, Carnegie Mellon University Jeremie Kim, ETH Zurich and Carnegie Mellon University Saugata Ghose, University of Illinois at Urbana–Champaign and Carnegie Mellon University Can Alkan, Bilkent University Onur Mutlu, ETH Zurich, Carnegie Mellon University, and Bilkent University Goal: Minimizing Alignment Time

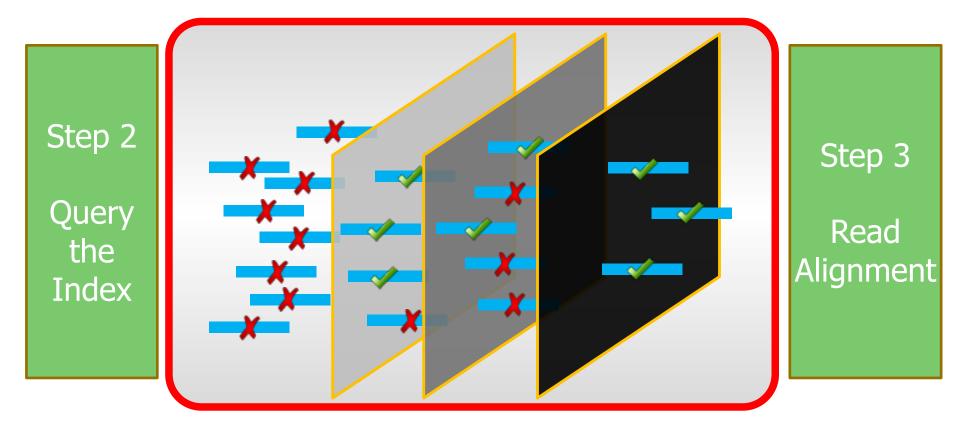
Sequence Alignment is expensive

Our goal is to accelerate read mapping by reducing the need for dynamic programming algorithms



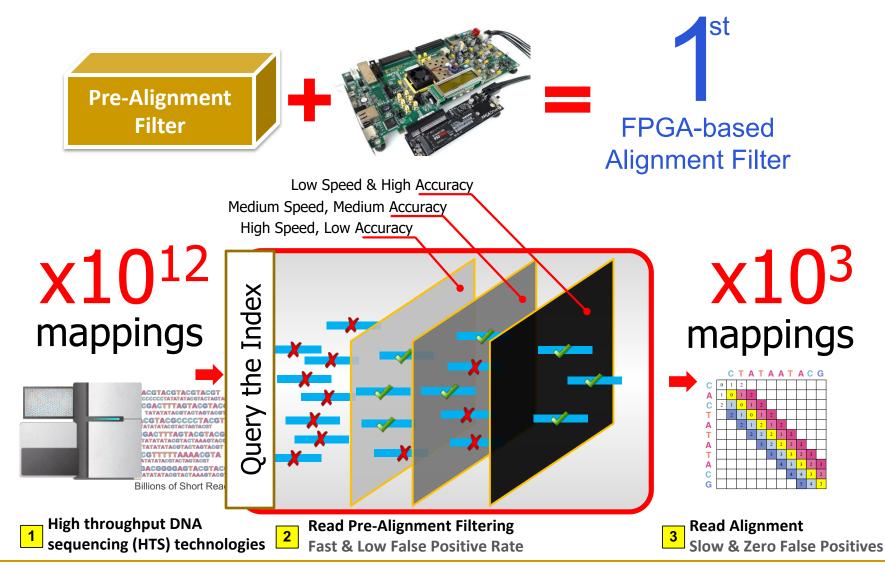


Ideal Filtering Algorithm



- 1. Filter out most of dissimilar sequences.
- 2. Preserve all similar sequences.
- 3. Do it quickly.

Proposed Solution: GateKeeper



GateKeeper

Key observation:

 If two strings differ by *E* edits, then every pairwise match can be aligned in at most 2*E* shifts.

Key ideas:

- Quickly find similar sequences using Hamming Distance.
- Compute "Shifted Hamming Distance": AND of 2E+1 Hamming vectors of two strings, to identify invalid mappings
- Use only **bit-parallel operations** that nicely map to:
 - SIMD instructions
 - FPGA
 - Logic layer of the 3D-stacked memory
 - In-memory accelerators (e.g., Ambit)

Mechanisms

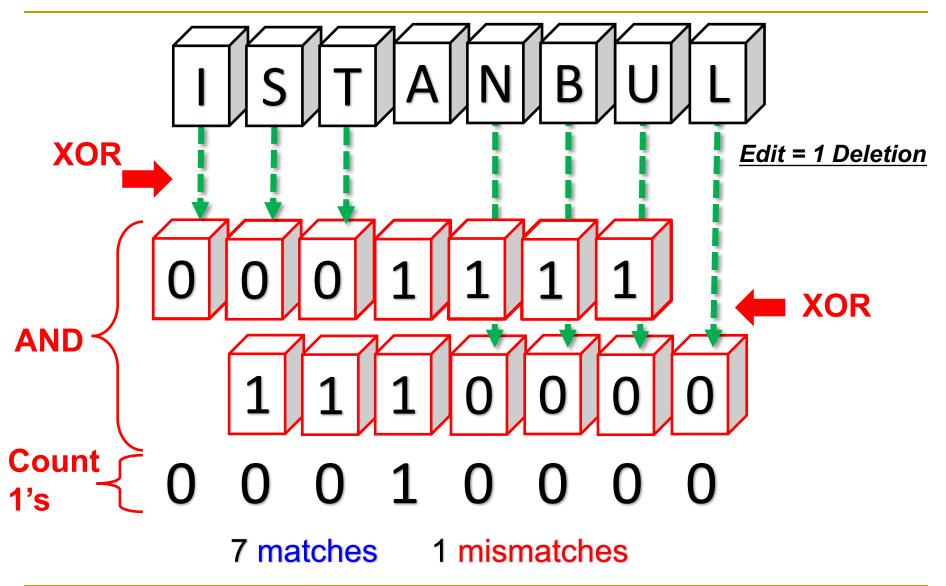
Key observation:

 If two strings differ by *E* edits, then every pairwise match can be aligned in at most 2*E* shifts.

Hamming Distance $(\Sigma \oplus)$

3 matches 5 mismatches Edit = 1 Deletion R P To cancel the effect of a deletion, we need to shift in the *right* direction

Shifted Hamming Distance (Xin+ 2015)



Effect of Errors on Sequence Alignment

Exact Matching:

TCCATTGACATTCGTGAGCTGCTCCTTCTCTCCCACCCCTTTGCCC TCCATTGACATTCGTGAGCTGCTCCTTCTCTCCCACCCCTTTGCCC

(a)

Substitution:

TCCATTGACACTCGTGAGCTGCACCTTCTCTCCCACCCCTTTGCCC TCCATTGACATTCGTGAGCTGCTCCTTCTCTCCCACCCCTTTGCCC

(b)

Insertion:

TCCATTGACAGTTCGTGAGCTGCTCCTTCTTCTCCCACCCCTTTGC TCCATTGACATTCGTGAGCTGCTCCTTCTCTCCCACCCCTTTGCCC

(c)

Deletion:

TCCATTGACATTCGGAGCTGCTCCTTCTCTCCACCCCTTTGCCCTT TCCATTGACATTCGTGAGCTGCTCCTTCTCTCCCACCCCTTTGCCC

(d)



Substitution, Deletion or \searrow 1-step shift ✓ match

2-step shift ∽ match

Mechanisms

Key observation:

 If two strings differ by *E* edits, then every pairwise match can be aligned in at most 2*E* shifts.

Key ideas:

Quickly find similar sequences using Hamming Distance.

 Compute "Shifted Hamming Distance": AND of 2*E*+1 Hamming vectors of two strings, to identify invalid mappings

GateKeeper Walkthrough

| Generate 2E+1 masks | Amend random zeros:AND all masks, $101 \rightarrow 111 \& 1001 \rightarrow 1111$ ACCEPT iff number of `1' \leq Threshold | |
|---|---|----------------------------------|
| Query Reference | :GAGAGAGATATTTAGTGTTGCAGCACTACAACACAAAAGAGGACCAACTTACGTGTCTAAAAGGGGGGAACATTGTTGGGCCGG [;] GAGAGAGATAGTTAGTGTTGCAGCCACTACAACACAAAAGAGGGACCAACTTACGTGTCTAAAAGGGGGAGACATTGTTGGGCCG | |
| 1-Deletion Mask 2-Deletion Mask 3-Deletion Mask 1-Insertion Mask 2-Insertion Mask | :111111111101110110011011101110010010010 | 00 10 11 10 00 |
| AND Mask | :0000000001000000000000100000000000000 | 0 |
| 1-I 2-I 3-I 1-Ir 2-Ir 3-Ir | al to track the diagonally consecutive matches in the neighborhood map. | DO LO L1 L0 D0 D0 |
| Needleman-Wunsch | GAGAGAGATATTTAGTGTTGCAG-CACTACAACACAAAAGAGGACCAACTTACGTGTCTAAAAAGGGGGGAACATTGTTGGGCCG | GG |

Needleman-Wunsch Alignment

GateKeeper

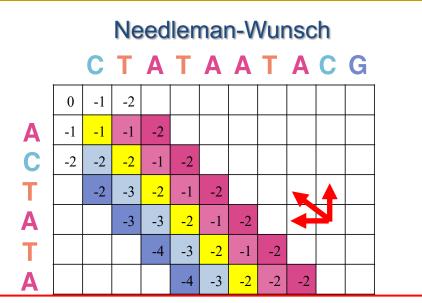
Key observation:

 If two strings differ by *E* edits, then every pairwise match can be aligned in at most 2*E* shifts.

Key ideas:

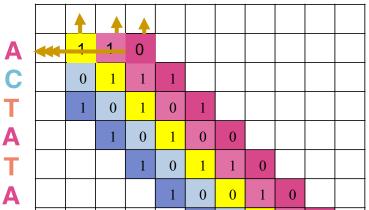
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- Use only **bit-parallel operations** that nicely map to:
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Alignment Matrix vs. Neighborhood Map



Neighborhood Map

C T A T A A T A C G

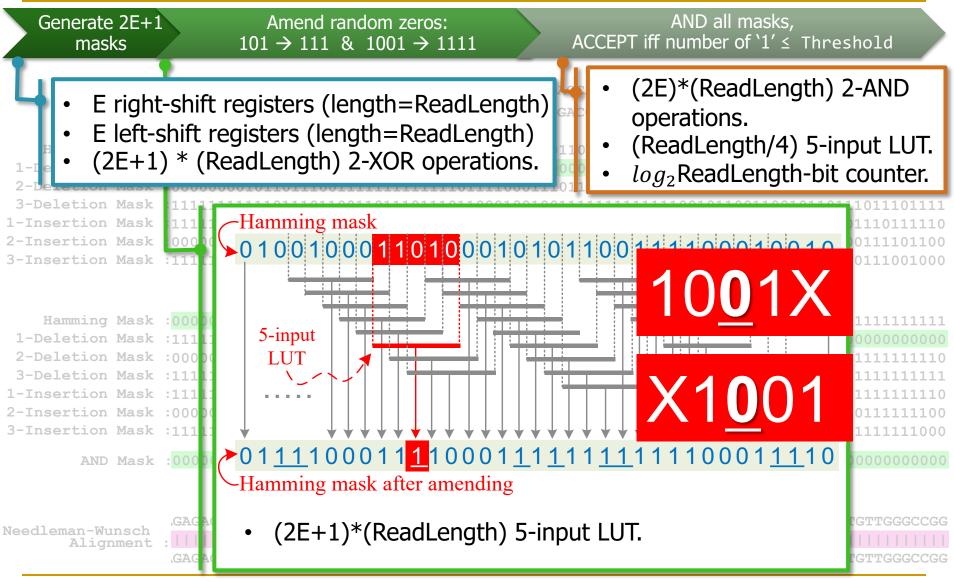


Independent vectors can be processed in parallel using hardware technologies

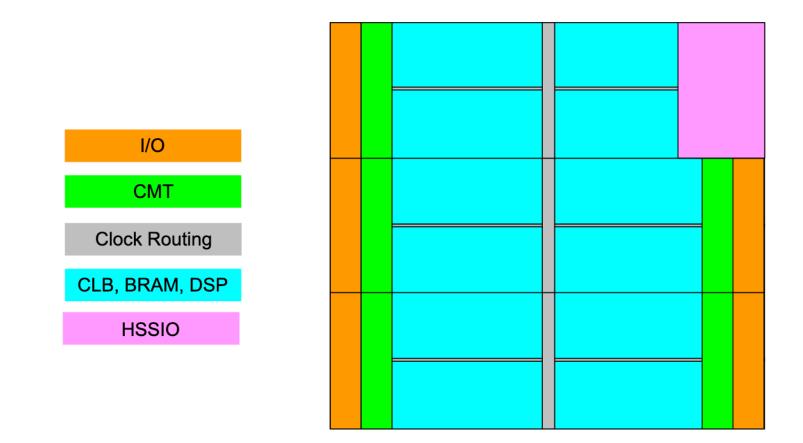


Hardware Architecture

GateKeeper Walkthrough (cont'd)

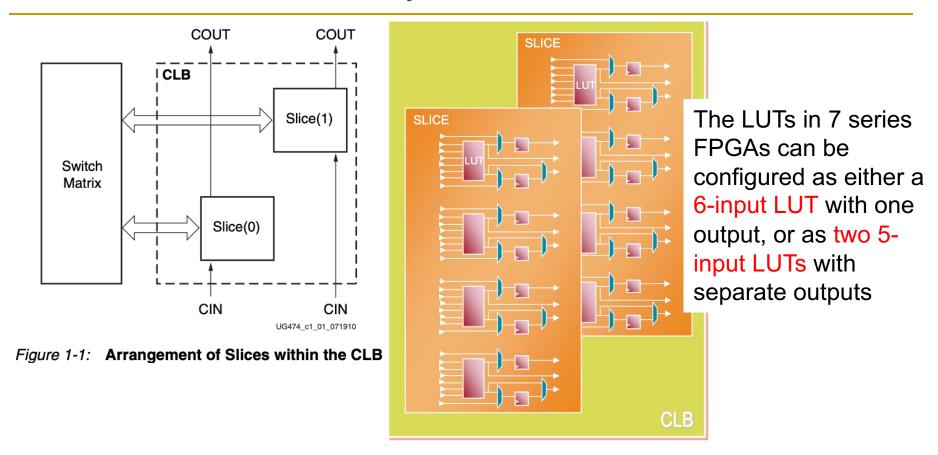


Virtex-7 FPGA Layout



Configurable logic blocks (CLBs) are the main logic resources for implementing sequential as well as combinatorial circuits

Virtex-7 FPGA Layout



| Table 2-1: | Logic | Resources | in | One C | ЪВ |
|------------|-------|-----------|----|-------|----|
|------------|-------|-----------|----|-------|----|

| Slices | LUTs | Flip-Flops | Arithmetic and Carry Chains | Distributed RAM ⁽¹⁾ | Shift Registers ⁽¹⁾ | |
|--------|------|------------|--------------------------------|--------------------------------|--------------------------------|--|
| 2 | 8 | 16 | 2 | 256 bits | 128 bits | |

"7 Series FPGAs Configurable Logic Block", User Guide, Xilinx 2016

Key Results: Methodology and Evaluation

Methodology

- System setup:
 - □ 3.6 GHz Intel i7-3820 (supports only PCIe 2.0)
 - □ Xilinx VC709 (~\$5000)
 - Architecture implementation using Vivado 2014.4 in Verilog
 - RIFFA 2.2 to perform Host-FPGA PCIe communication



- Evaluated dataset:
 - Real sequencing read set (ERR240727_1.fastq)
 - Five simulated read sets of 100 bp and 300 bp long Illuminalike reads with different type and number of edits.

Prior Work on Pre-Alignment Filtering

- Adjacency Filter (BMC Genomics, 2013)
 - Slow
 - Accepts a large number of dissimilar sequences.
- Shifted Hamming Distance (SHD) (*Bioinformatics, 2015*)
 - □ It requires the same execution time as the Adjacency Filter
 - It accepts 4X fewer dissimilar sequences compared to the Adjacency Filter.
 - □ It suffers from a limited sequence length (\leq 128 bp)

VC709 Resource Utilization

Theoretically:

- Up to 140 GateKeeper Processing cores on a single FPGA (E=5, 100bp)
- BUT bottlenecked by PCIe bandwidth
- Small area allows integration into FPGAs already inside of sequencers

Table 2. FPGA resource utilization for a single GateKeeper core

| | Resource utilization % | | | | |
|---|------------------------|----------------|----------------|---------------|----------------|
| Read length | 100 bp | | 300 bp | | |
| Edit distance | 2 | 5 | 2 | 5 | 15 |
| Slice LUT ^a Slice Register ^b | 0.39% 0.01% | 0.71% 0.01% | 1.27% 0.01% | 2.2% 0.01% | 4.82% 0.01% |

^aLUT: look-up tables. ^bFlip-flop.

VC709 Resource Utilization

Experimentally:

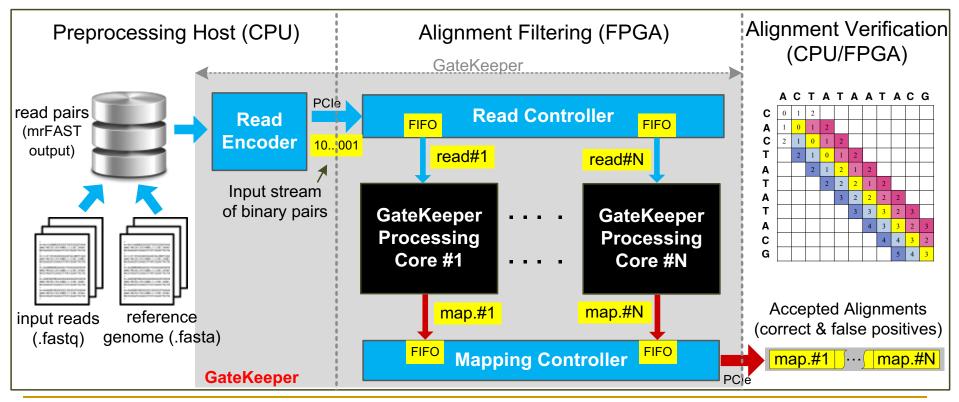
 GateKeeper aligns each read against up to 8 and 16 different reference segments in parallel, without violating the timing constraints for a sequence lengths of 300 and 100 bp, respectively.

Table 3. Overall system resource utilization under different readlengths and edit distance thresholds

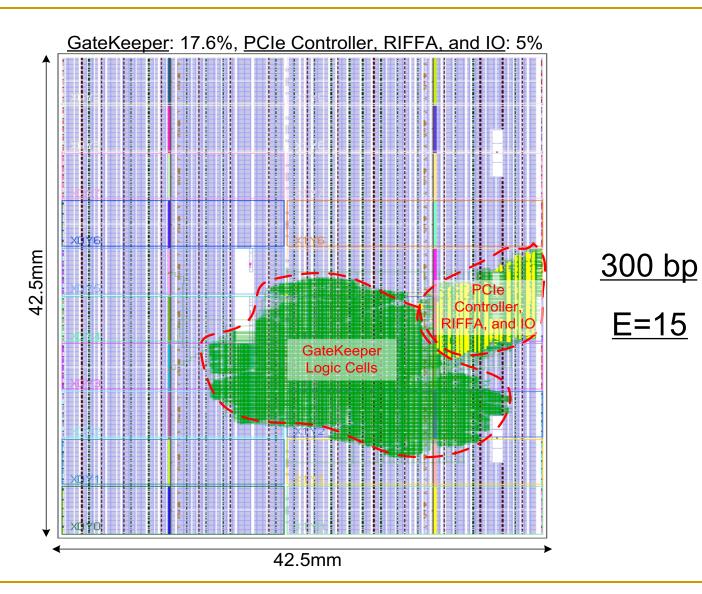
| | Resource utilization % | | | | |
|----------------|------------------------|------------|----------------------------|------------------------------|--|
| Read length | 100 bp 16 GateKe | eper cores | 300 bp <u>8 GateKee</u> | 300 bp 8 GateKeeper cores | |
| Edit distance | 2 | 5 | 2 | 15 | |
| Slice LUT | 32% | 45% | 50% | 69% | |
| Slice register | 2% | 2% | 17% | 91% | |
| Block memory | 2% | 2% | 2% | 2% | |

GateKeeper Accelerator Architecture

- Maximum data throughput =~13.3 billion bases/sec
- Can examine 8 (300 bp) or 16 (100 bp) mappings concurrently at 250 MHz
- Occupies 50% (100 bp) to 91% (300 bp) of the FPGA slice LUTs and registers



FPGA Chip Layout



Speed & Accuracy Results 90x-130x faster

than SHD (Xin et al., 2015) and the Adjacency Filter (Xin et al., 2013).

Accepts 4x fewer dissimilar strings

than the Adjacency Filter (Xin et al., 2013).

10x speedup

with the addition of GateKeeper to the mrFAST mapper (Alkan et al., 2009).

Freely available online

github.com/BilkentCompGen/GateKeeper

GateKeeper Conclusions

- There is a significant performance gap between highthroughput DNA sequencers and read mapper
- Sequence alignment is computationally expensive and unavoidable
- GateKeeper is the first hardware accelerator architecture (as a pre-alignment filter) for quickly rejecting dissimilar sequences
- It provides a huge speedup of up to 130x compared to the previous state of the art software solution.

GateKeeper Conclusions

- FPGA-based pre-alignment filtering greatly speeds up read mapping
 - 10x speedup of a state-of-the-art mapper (mrFAST)

- FPGA-based pre-alignment can be integrated with the sequencer
 - □ It can help to hide the complexity and details of the FPGA
 - Enables real-time filtering while sequencing

More on SHD (SIMD Implementation)

- Download and test for yourself
- <u>https://github.com/CMU-SAFARI/Shifted-Hamming-Distance</u>

Bioinformatics, 31(10), 2015, 1553–1560 doi: 10.1093/bioinformatics/btu856 Advance Access Publication Date: 10 January 2015 Original Paper

OXFORD

Sequence analysis

Shifted Hamming distance: a fast and accurate SIMD-friendly filter to accelerate alignment verification in read mapping

Hongyi Xin^{1,}*, John Greth², John Emmons², Gennady Pekhimenko¹, Carl Kingsford³, Can Alkan^{4,}* and Onur Mutlu^{2,}*

More on GateKeeper

 Download and test for yourself <u>https://github.com/BilkentCompGen/GateKeeper</u>

Bioinformatics

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Article Navigation

GateKeeper: a new hardware architecture for accelerating pre-alignment in DNA short read mapping 🚥

Mohammed Alser 🖾, Hasan Hassan, Hongyi Xin, Oğuz Ergin, Onur Mutlu 🖾, Can Alkan 🖾

Bioinformatics, Volume 33, Issue 21, 01 November 2017, Pages 3355–3363, https://doi.org/10.1093/bioinformatics/btx342 Published: 31 May 2017 Article history ▼

Alser+, <u>"GateKeeper: A New Hardware Architecture for Accelerating Pre-Alignment in DNA Short Read Mapping"</u>, Bioinformatics, 2017.

Strengths

- New and simple solution to a critical problem. New algorithm and hardware architecture.
- GateKeeper does not sacrifice any of the aligner capabilities, as it does not modify or replace the alignment step.
- Design is scalable; could add more processing cores in the future.
- Some sequencers use FPGAs as well, so GateKeeper could be integrated into them.

Strengths (cont'd)

- Authors understand and highlight limitations of GateKeeper
- Greatly improves filtering speed and accuracy
- Spurred quite a few papers that build on GateKeeper
- Well-written, interesting and easy to understand paper

Weaknesses

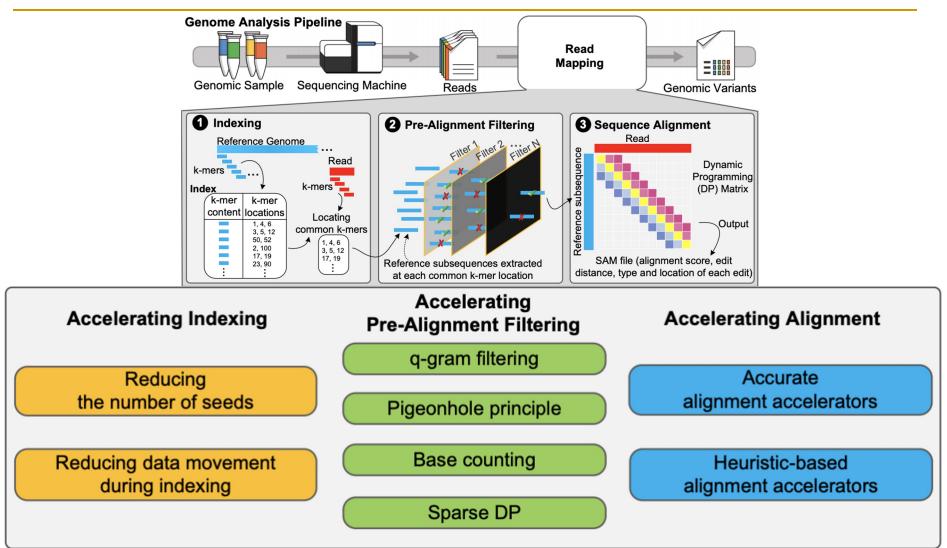
- The benefits of such a mechanism require an FPGA and advanced knowledge with computers, this may be problematic for some biologists/genomicists/geneticists
- The amendment of the random zeros is a simple "hack" to reduce the number of false positives, but there is no explanation why GateKeeper only flips the patterns 101 and 1001, what about 10001? And 10ⁿ1?
- The paper can be confusing at times due to the use of a "supplementary material" document that is constantly referred to (but understandable as there was a page limit set by the publication journal).

Weaknesses (cont'd)

- GateKeeper's accuracy degrades exponentially for *E* >2%, and becomes ineffective for *E* >8%.
- GateKeeper is tested using short reads
 - 3rd generation sequencing machines produce much longer reads

Thoughts and Ideas

Accelerating Read Mapping



Alser+, "Accelerating Genome Analysis: A Primer on an Ongoing Journey", IEEE Micro, 2020.

Our Ongoing Journey

Specialized Pre-alignment Filtering Near-memory/In-memory **Pre-alignment Filtering** Accelerators (GPU, FPGA) **GRIM-Filter** [BMC Genomics'18] GateKeeper [Bioinformatics'17] SneakySnake [IEEE Micro'21] MAGNET [AACBB'18] GenASM [MICRO 2020] Shouji [Bioinformatics'19] GateKeeper-GPU [arXiv'21] Near-memory Sequence Alignment SneakySnake [Bioinformatics'20] GenASM [MICRO 2020] Sequencing Machine Storage (SSD/HDD) Main Memory Microprocessor

Key Takeaways

- A novel method to accelerate Sequence Alignment in genome analysis.
- Simple and effective
- Hardware/software cooperative
- Good potential for work building on it to extend it
 - To make things more efficient and effective
 - Multiple works have already built on the paper (see MAGNET, Shouji, GRIM-Filter, SneakySnake, GenCache)
- Easy to read and understand paper

Adoption of hardware accelerators in genome analysis

Bioinformatics: Reviewer #6 (Dec. 2016)

I have a major concern with the work that is actually not a problem with the manuscript at all. Specifically, I have the concern that there has been little to no adoption of previous specialized hardware solutions related to improving the speed of alignment. While there has been considerable work in this area (which the authors do an admirable job of citing), it does not seem that these hardware-based solutions have gained any type of real traction in the community, as the vast majority of alignment is still performed on "regular" CPUs, where the extent of hardware acceleration is the adoption of specific SIMD or vectorized instructions. While I don't think that this practical concern should preclude publication of the current work, it is something worth considering (e.g. what, if any, of the proposed improvements to the SHD filter could be "back-ported" to a software-only 46 solution).

Our Response

We see the reviewer's point, but we do not believe this should be held against the research in the area of FPGA-based acceleration of read mapping in particular or genomics in general. It always takes time to adopt a "new" or "different" hardware technology since it requires investment into the hardware infrastructure. The main challenges/barriers that limit the popularity of FPGAs in the genomics field are the high cost, design effort, and development time. Due to the fact that the deliverable of such projects is normally a hardware product, researchers tend to commercialize their research with startup companies and engage themselves with industrial collaborators, as we describe below. Today, the cost structure of FPGAs is changing because major cloud infrastructures (e.g., by Microsoft Azure and Amazon AWS) offer FPGAs as core engines of the infrastructure. Therefore, we believe the benefits of FPGA-based acceleration has become available to many more folks in the community, especially with the open-source release of such FPGA-accelerated solutions. To increase adoption, we have decided to release our source code for GateKeeper. It is available on https://github.com/BilkentCompGen/GateKeeper.

Some examples of the research groups that commercialize their research and promote FPGA-based or even cloudbased products for genomics are as follows:

http://www.timelogic.com/catalog/775 http://www.gidel.com/HPC-RC/HPC-Applications.asp http://www.edicogenome.com/dragen_bioit_platform/the-dragen-engine-2/ http://www.bcgsc.ca/platform/bioinfo/software/XpressAlign/releases/1.0 https://www.sevenbridges.com/amazon/ http://www.falcon-computing.com/index.php/solutions/falcon-genomics-solutions/

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Our Response (cont'd)

It is also important to emphasize that the necessity of designing a mapper on hardware is currently steering the field towards more personalized medicine. Hardware-accelerated mappers (using various platforms such as SIMD, GPUs, and FPGAs) are becoming increasingly popular as they can be potentially directly integrated into sequencing machines (the Illumina sequencer, for example, includes an FPGA chip inside it

https://support.illumina.com/content/dam/illumina-support/documents/downloads/software/hiseq/hcs_2-0-12/installnotes_hcs2-0-12.pdf), such that we have a single machine that can perform both sequencing and mapping (Lindner, et al., Bioinformatics 2016). This approach has two benefits. First, it can hide the complexity and details of the underlying hardware from users who are not necessarily aware about FPGAs (e.g., biologists and mathematicians). Second, it allows a significant reduction in total genome analysis time by starting read mapping while still sequencing. Hence, an end user or researcher in genomics might not directly deal with the "pre-alignment on FPGA" or "mapper on FPGA", but they might purchase a sequencer that performs pre-alignment and alignment using FPGAs inside. As such, one potential target of our research is to influence the design of more intelligent sequencing machines by integrating GateKeeper inside them.

In fact, we believe GateKeeper is very suitable to be used as part of a sequencer as it provides a complete prealignment system that includes many processing cores, where all processing cores work in parallel to provide extremely fast filtering. We believe such a fast approach can make sequencers more intelligent and attractive.

Remember What We Said in the First Lecture

Dream and, they will come

•Computing landscape is very different from 10-20 years ago.

•As applications push boundaries, computing platforms will become increasingly strained.

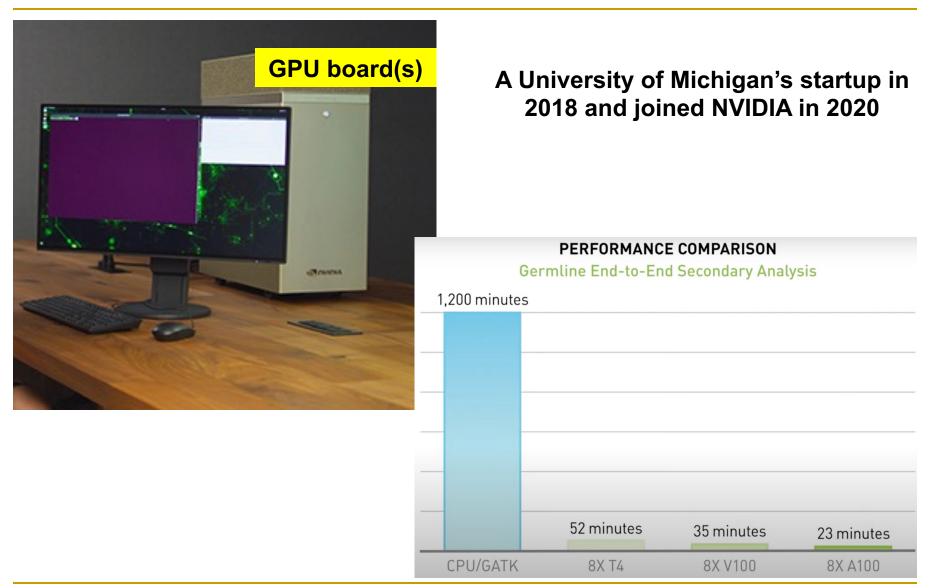
Illumina DRAGEN Bio-IT Platform (2018)

Processes whole genome at 30x coverage in ~25 minutes with hardware support for data compression



emea.illumina.com/products/by-type/informatics-products/dragen-bio-it-platform.html emea.illumina.com/company/news-center/press-releases/2018/2349147.html

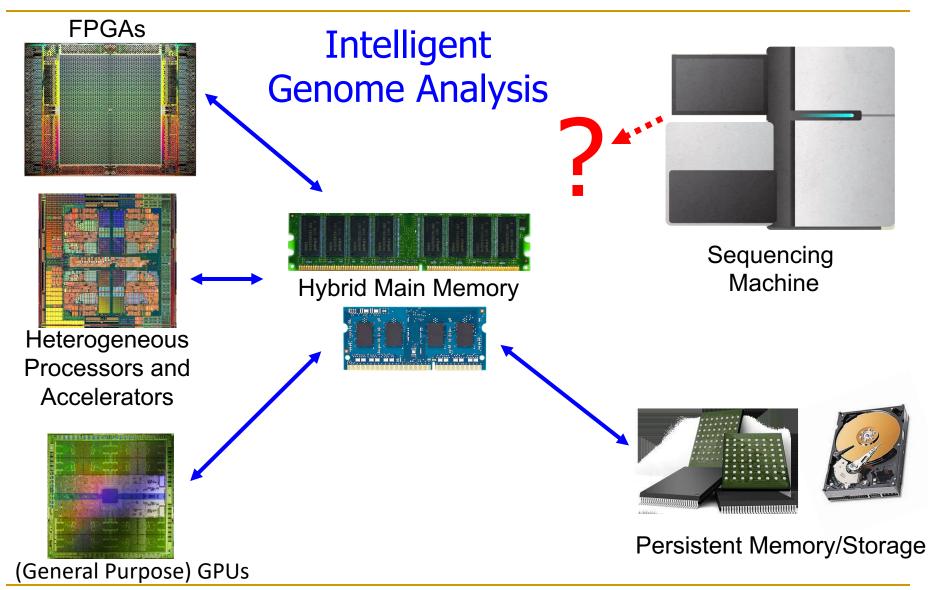
NVIDIA Clara Parabricks (2020)



https://developer.nvidia.com/clara-parabricks

Computing is Still Bottlenecked by Data Movement

Processing Genomic Data Where it Makes Sense



Most speedup comes from parallelism enabled by novel architectures and algorithms

More on GateKeeper [Alser+, Bioinformatics 2017]

<u>Mohammed Alser</u>, Hasan Hassan, Hongyi Xin, Oguz Ergin, Onur Mutlu, and Can Alkan <u>"GateKeeper: A New Hardware Architecture for Accelerating Pre-Alignment in</u> DNA Short Read Mapping"

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[Source Code]

[Online link at Bioinformatics Journal]

Bioinformatics



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Bioinformatics, Volume 33, Issue 21, 01 November 2017, Pages 3355–3363,

https://doi.org/10.1093/bioinformatics/btx342

Published: 31 May 2017 Article history •

Read Mapping in 111 pages!

In-depth analysis of 107 read mappers (1988-2020)

Mohammed Alser, Jeremy Rotman, Dhrithi Deshpande, Kodi Taraszka, Huwenbo Shi, Pelin Icer Baykal, Harry Taegyun Yang, Victor Xue, Sergey Knyazev, Benjamin D. Singer, Brunilda Balliu, David Koslicki, Pavel Skums, Alex Zelikovsky, Can Alkan, Onur Mutlu, Serghei Mangul

"<u>Technology dictates algorithms: Recent developments in read alignment</u>" Genome Biology, 2021 [Source code]

Alser et al. Genome Biology (2021) 22:249 https://doi.org/10.1186/s13059-021-02443-7

REVIEW



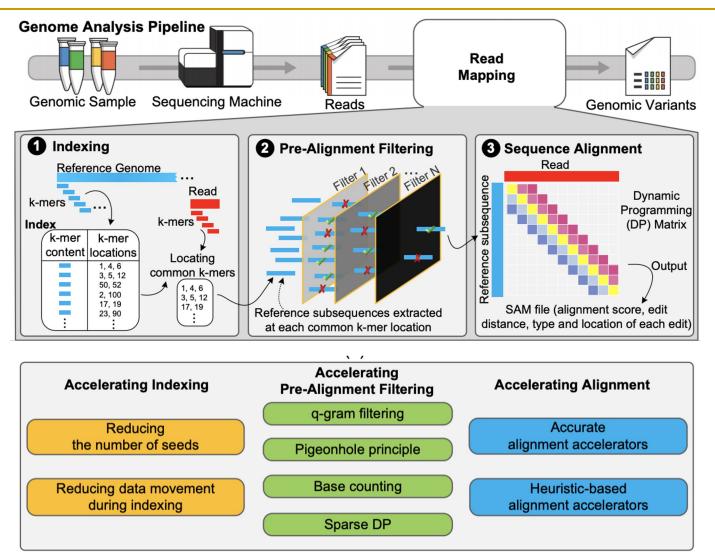
Open Access

Check fo updates

Technology dictates algorithms: recent developments in read alignment

Mohammed Alser^{1,2,3†}, Jeremy Rotman^{4†}, Dhrithi Deshpande⁵, Kodi Taraszka⁴, Huwenbo Shi^{6,7}, Pelin Icer Baykal⁸, Harry Taegyun Yang^{4,9}, Victor Xue⁴, Sergey Knyazev⁸, Benjamin D. Singer^{10,11,12}, Brunilda Balliu¹³, David Koslicki^{14,15,16}, Pavel Skums⁸, Alex Zelikovsky^{8,17}, Can Alkan^{2,18}, Onur Mutlu^{1,2,3†} and Serghei Mangul^{5*†}

Accelerating Read Mapping



Alser+, "Accelerating Genome Analysis: A Primer on an Ongoing Journey", IEEE Micro, 2020.

Detailed Analysis of Tackling the Bottleneck

Mohammed Alser, Zülal Bingöl, Damla Senol Cali, Jeremie Kim, Saugata Ghose, Can Alkan, Onur Mutlu <u>"Accelerating Genome Analysis: A Primer on an Ongoing Journey"</u>

IEEE Micro, August 2020.





Home / Magazines / IEEE Micro / 2020.05

IEEE Micro

Accelerating Genome Analysis: A Primer on an Ongoing Journey

Sept.-Oct. 2020, pp. 65-75, vol. 40 DOI Bookmark: 10.1109/MM.2020.3013728

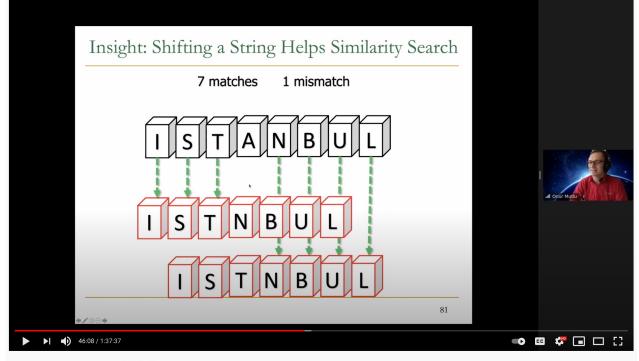
Authors

Mohammed Alser, ETH Zürich Zulal Bingol, Bilkent University Damla Senol Cali, Carnegie Mellon University Jeremie Kim, ETH Zurich and Carnegie Mellon University Saugata Ghose, University of Illinois at Urbana–Champaign and Carnegie Mellon University Can Alkan, Bilkent University Onur Mutlu, ETH Zurich, Carnegie Mellon University, and Bilkent University

More on Fast Genome Analysis ...

 Onur Mutlu, <u>"Accelerating Genome Analysis: A Primer on an Ongoing Journey"</u> *Invited Lecture at <u>Technion</u>*, Virtual, 26 January 2021. [<u>Slides (pptx) (pdf)</u>] [<u>Talk Video (1 hour 37 minutes, including Q&A)</u>] [<u>Related Invited Paper (at IEEE Micro. 2020)</u>]

[Related Invited Paper (at IEEE Micro, 2020)]

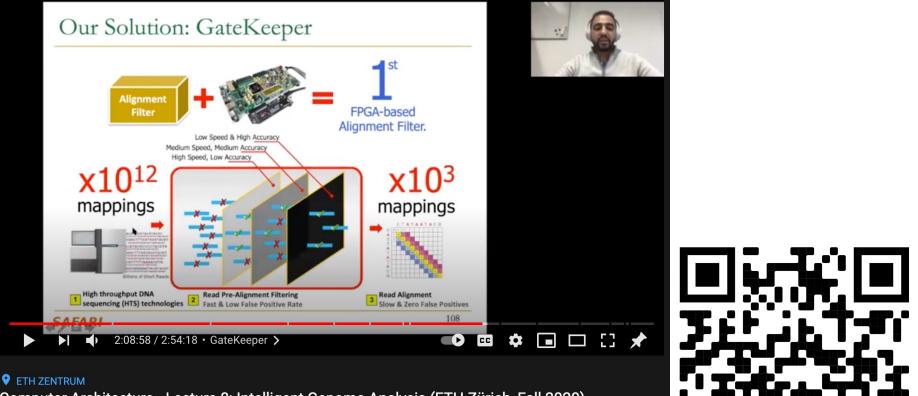


Onur Mutlu - Invited Lecture @Technion: Accelerating Genome Analysis: A Primer on an Ongoing Journey

566 views • Premiered Feb 6, 2021



More on Intelligent Genome Analysis ...



Computer Architecture - Lecture 8: Intelligent Genome Analysis (ETH Zürich, Fall 2020)

https://www.youtube.com/watch?v=ygmQpdDTL7o

Detailed Lectures on Genome Analysis

- Computer Architecture, Fall 2020, Lecture 3a
 - Introduction to Genome Sequence Analysis (ETH Zürich, Fall 2020)
 - https://www.youtube.com/watch?v=CrRb32v7SJc&list=PL5Q2soXY2Zi9xidyIgBxUz7 xRPS-wisBN&index=5
- Computer Architecture, Fall 2020, Lecture 8
 - **Intelligent Genome Analysis** (ETH Zürich, Fall 2020)
 - https://www.youtube.com/watch?v=ygmQpdDTL7o&list=PL5Q2soXY2Zi9xidyIgBxU z7xRPS-wisBN&index=14
- Computer Architecture, Fall 2020, Lecture 9a
 - **GenASM: Approx. String Matching Accelerator** (ETH Zürich, Fall 2020)
 - https://www.youtube.com/watch?v=XoLpzmN-Pas&list=PL5Q2soXY2Zi9xidyIgBxUz7xRPS-wisBN&index=15
- Accelerating Genomics Project Course, Fall 2020, Lecture 1
 - Accelerating Genomics (ETH Zürich, Fall 2020)
 - https://www.youtube.com/watch?v=rgjl8ZyLsAg&list=PL5Q2soXY2Zi9E2bBVAgCqL gwiDRQDTyId

https://www.youtube.com/onurmutlulectures

Prior Research on Genome Analysis (1/2)

- Alser + <u>"SneakySnake: A Fast and Accurate Universal Genome Pre-Alignment Filter for CPUs, GPUs, and FPGAs.</u>", *Bioinformatics*, 2020.
- Senol Cali+, "<u>GenASM: A High-Performance, Low-Power Approximate</u> <u>String Matching Acceleration Framework for Genome Sequence Analysis</u>", *MICRO* 2020.
- Alser+, "<u>Technology dictates algorithms: Recent developments in read</u> <u>alignment</u>", *arXiv*, 2020.
- Kim+, "<u>AirLift: A Fast and Comprehensive Technique for Translating</u> <u>Alignments between Reference Genomes</u>", *arXiv*, 2020
- Alser+, "<u>Accelerating Genome Analysis: A Primer on an Ongoing Journey</u>", *IEEE Micro*, 2020.

Prior Research on Genome Analysis (2/2)

- Firtina+, "<u>Apollo: a sequencing-technology-independent, scalable and</u> <u>accurate assembly polishing algorithm</u>", *Bioinformatics*, 2019.
- Alser+, <u>"Shouji: a fast and efficient pre-alignment filter for sequence alignment</u>", *Bioinformatics* 2019.
- Kim+, "<u>GRIM-Filter: Fast Seed Location Filtering in DNA Read Mapping</u> <u>Using Processing-in-Memory Technologies</u>", *BMC Genomics*, 2018.
- Alser+, <u>"GateKeeper: A New Hardware Architecture for Accelerating</u> <u>Pre-Alignment in DNA Short Read Mapping</u>", *Bioinformatics*, 2017.
- Alser+, "<u>MAGNET: understanding and improving the accuracy of genome pre-alignment filtering</u>", *IPSI Transaction*, 2017.

GateKeeper: A New Hardware Architecture for Accelerating Pre-Alignment in DNA Short Read Mapping

Mohammed Alser, Hasan Hassan, Hongyi Xin, Oğuz Ergin, Onur Mutlu, Can Alkan **Bioinformatics, 2017**

Presented by: Mohammed Alser

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P&S Accelerating Genomics Lecture 5: GateKeeper





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